

Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
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EFS ID: 56645  
Application ID: 10613246  
Title of Invention: Integrated Circuit Memory  
Devices Having Clock Signal  
Arbitration Circuits Therein and  
Methods of Performing Clock  
Signal Arbitration  
First Named Inventor: Mario Au  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2003-07-03  
Effective Receipt Date: 2004-03-05  
Submission Type: Information Disclosure  
Statement  
Filing Type:  
Confirmation number: 4741  
Attorney Docket Number: 5646-42XX



Total Fees Authorized:


Digital Certificate Holder: cn=Grant J. Scott,ou=Registered Attorneys,ou=Patent and Trademark  
Office,ou=Department of Commerce,o=U.S. Government,c=US  
Certificate Message Digest: 4662a01a992c574628ea11bd380c6d9e9b265156



## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0


<b>Title of Invention</b>	Integrated Circuit Memory Devices Having Clock Signal Arbitration Circuits Therein and Methods of Performing Clock Signal Arbitration									
Application Number: 10/613246 										
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Grant J. Scott Registered Number: 36925</td><td>/gjs/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Grant J. Scott Registered Number: 36925	/gjs/	Attorney		
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us-ids	5646-42XX-usidst.xml									
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Comments										



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

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<p>Application Number: 10/613246 </p> <p>Confirmation Number: 4741</p> <p>First Named Applicant: Mario Au</p> <p>Attorney Docket Number: 5646-42XX</p> <p>Art Unit: 2818</p> <p>Search string: ( 6557053 or 6366529 or 6259648 or 5442747 or 4888741 ).pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6557053</td><td>2003-04-29</td><td>Bass et al.</td><td>B1</td><td>710</td><td>29</td></tr><tr><td></td><td>2</td><td>6366529</td><td>2002-04-02</td><td>Williams et al.</td><td>B1</td><td>365</td><td>239</td></tr><tr><td></td><td>3</td><td>6259648</td><td>2001-07-10</td><td>Kragick</td><td>B1</td><td>365</td><td>230.05</td></tr><tr><td></td><td>4</td><td>5442747</td><td>1995-08-15</td><td>Chan et al.</td><td></td><td>395</td><td>164</td></tr><tr><td></td><td>5</td><td>4888741</td><td>1989-12-19</td><td>Malinowski</td><td></td><td>365</td><td>230.05</td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6557053	2003-04-29	Bass et al.	B1	710	29		2	6366529	2002-04-02	Williams et al.	B1	365	239		3	6259648	2001-07-10	Kragick	B1	365	230.05		4	5442747	1995-08-15	Chan et al.		395	164		5	4888741	1989-12-19	Malinowski		365	230.05	Examiner Name	Date		
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